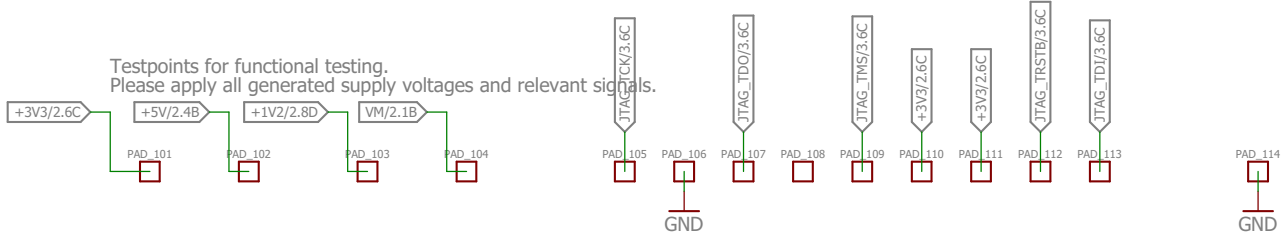
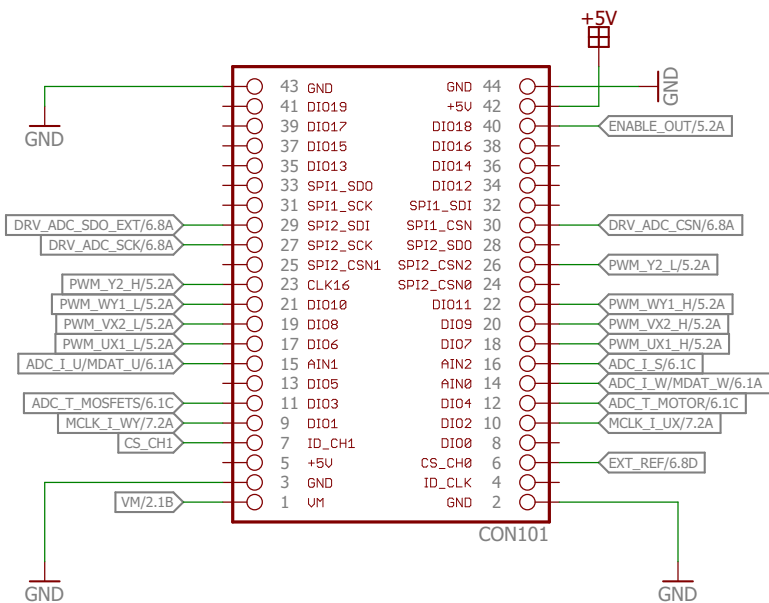
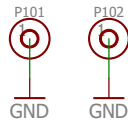


Project History

- V1.0 (SL, 2017-09-01)
New design based on
TMC8670-DEV Board
- V1.1 (SL, 2017-10-23)
- removed GND test points
 - changed 3.3V and 1.2V linear regulators
 - 1.2V regulator now on 3.3 instead of 5V rail
 - removed GPIO-LEDs
 - GPIO0-2 replaced by other signals, updated symbol, changed labels
 - connected analog signals to SPI ADC and DS ADCs
 - added labels (bottom) for UARTs
 - Enlarged exposed copper around mounting holes

Pins for measuring/interconnecting
Please apply external interconnections like SPI, SW prior and relevant signals for measurment if pins left.
Remove pins if not used or to group signals. Replace by more way connectors if necessary but please keep pitch.



TMC8670-EVAL I/O

Top Fiducials

- (5.0;9.0)
- (74.0;9.0)
- (74.0;72.0)

Bottom Fiducials

- (5.0;9.0)
- (74.0;9.0)
- (74.0;72.0)

TMC8670 Fiducials

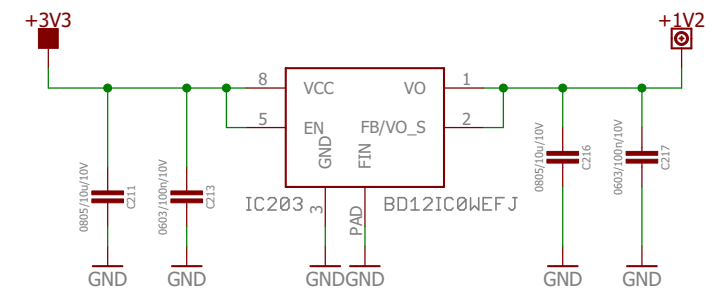
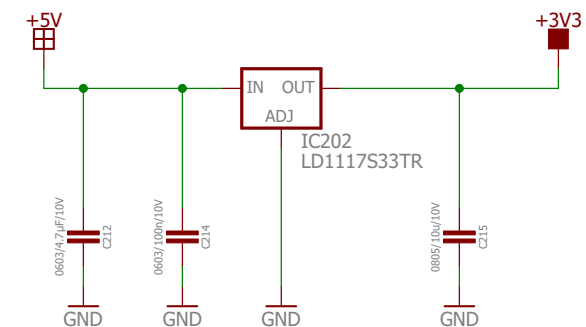
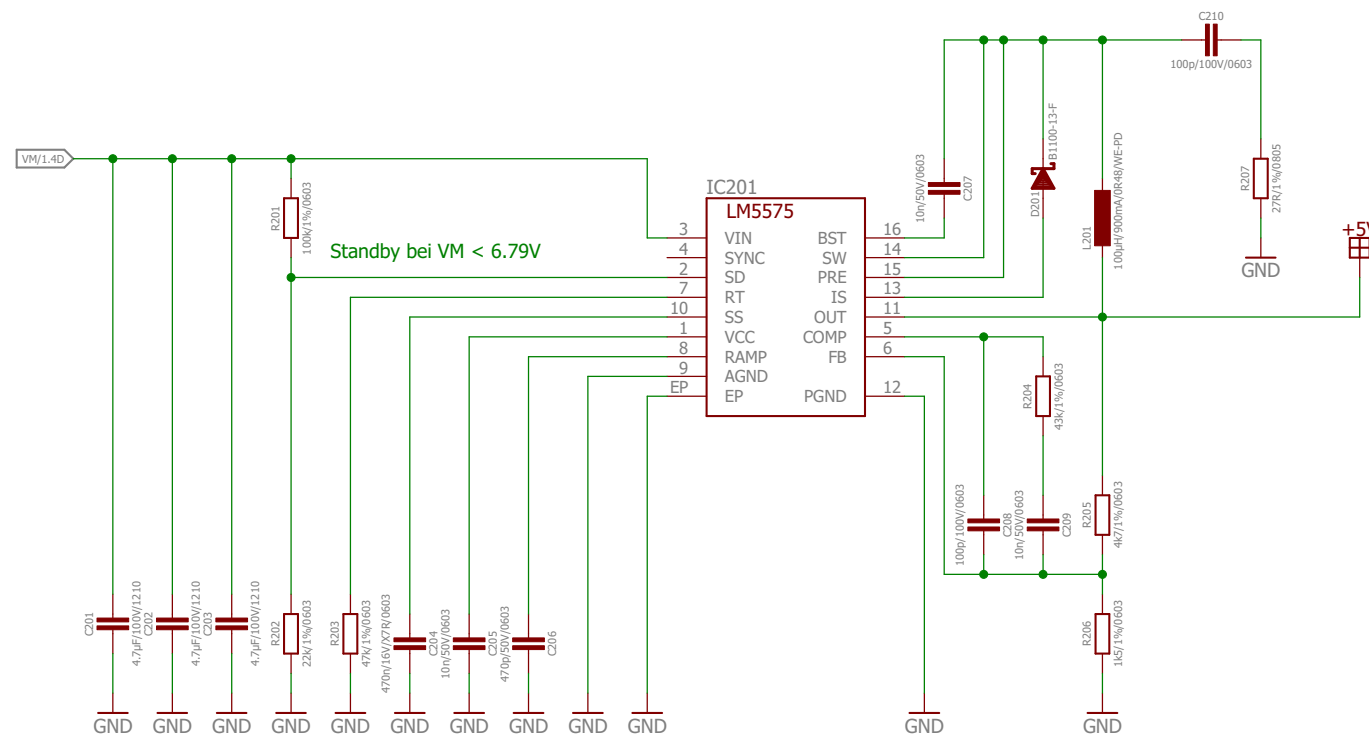
- (33.0;51.5)
- (46.0;38.5)

TMC8670 center at (39.5;45)



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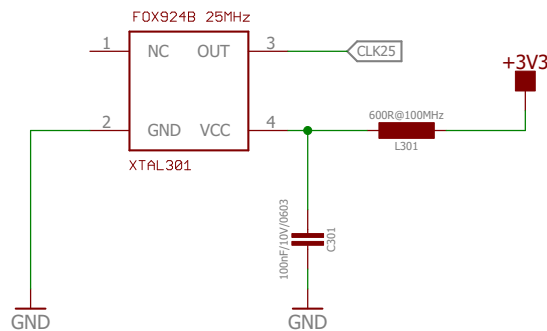
TMC8670-Eval_U11
23.10.2017 16:02
Sheet: 1/8



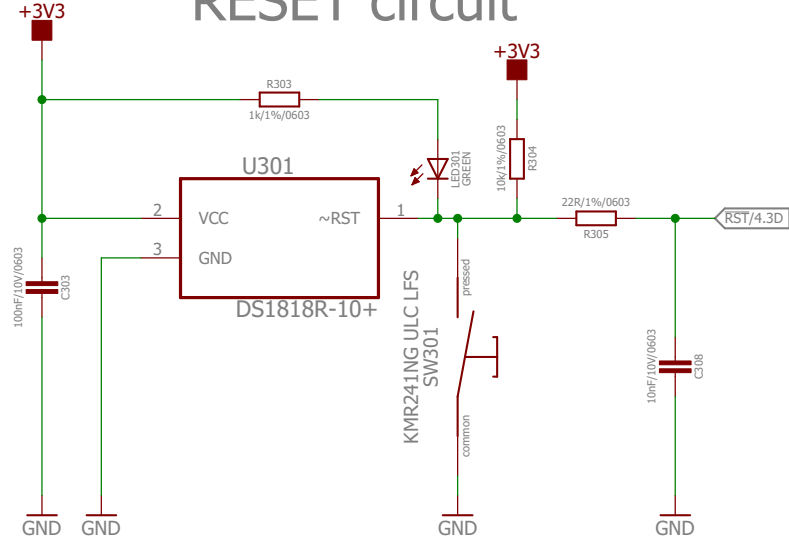
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TMC8670-Eval_U11
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Sheet: 2/8

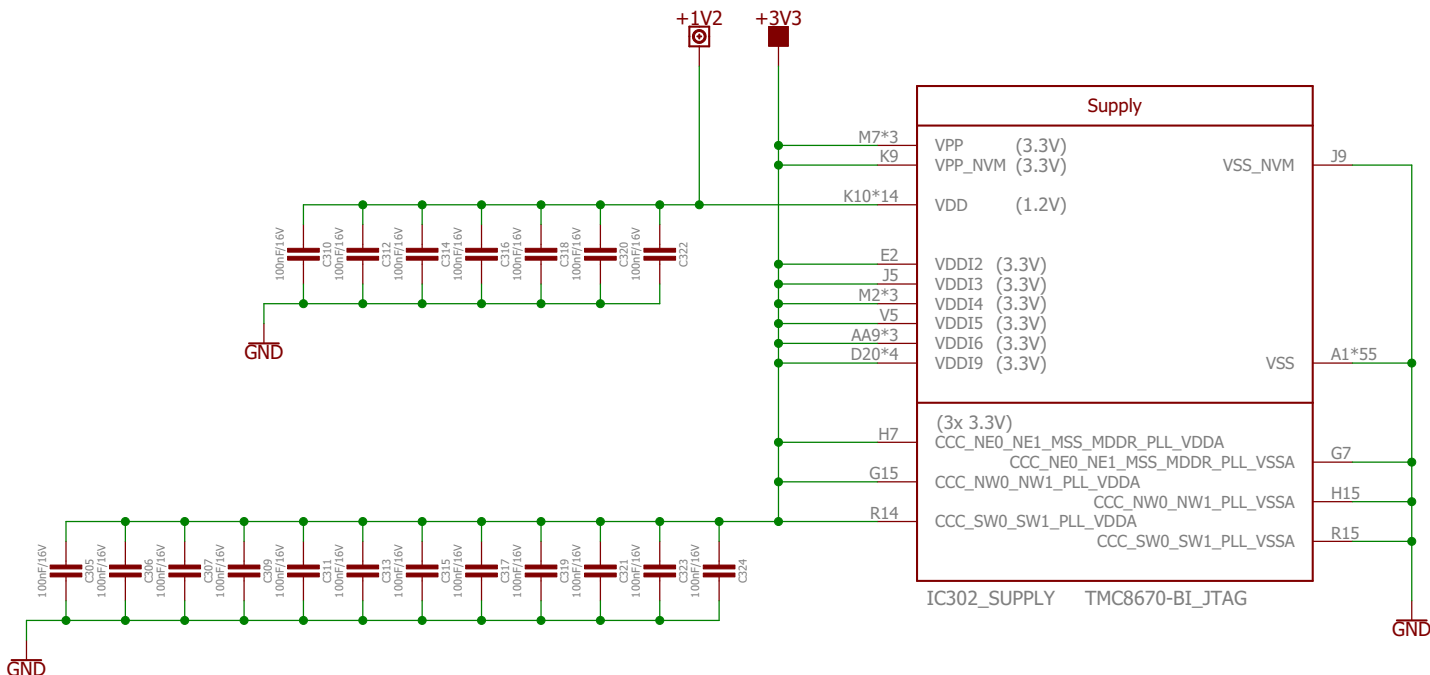
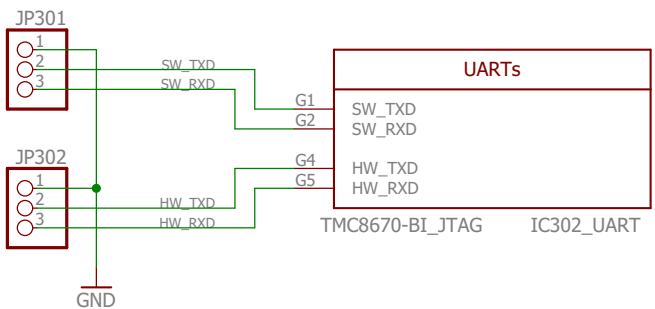
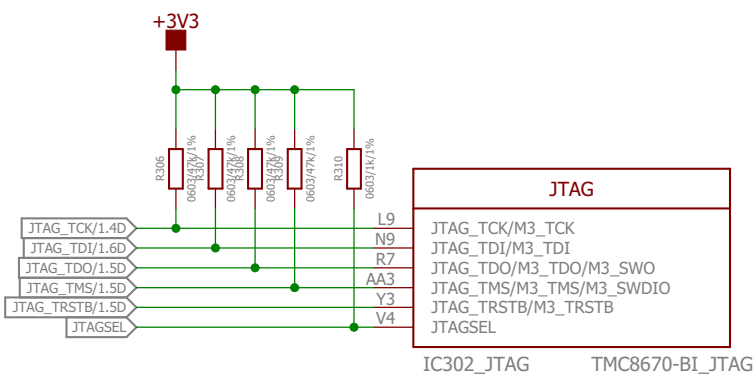
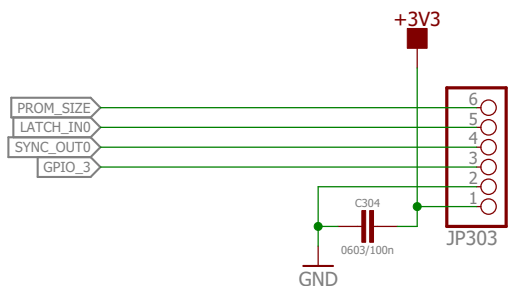
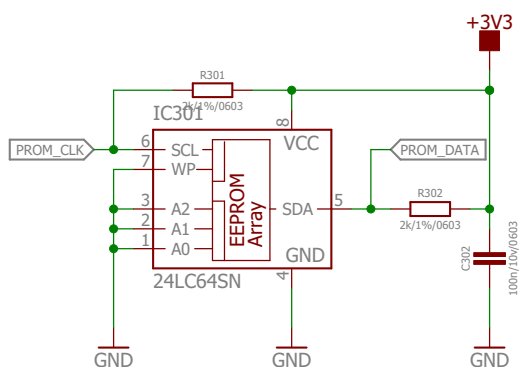
25Mhz clock source

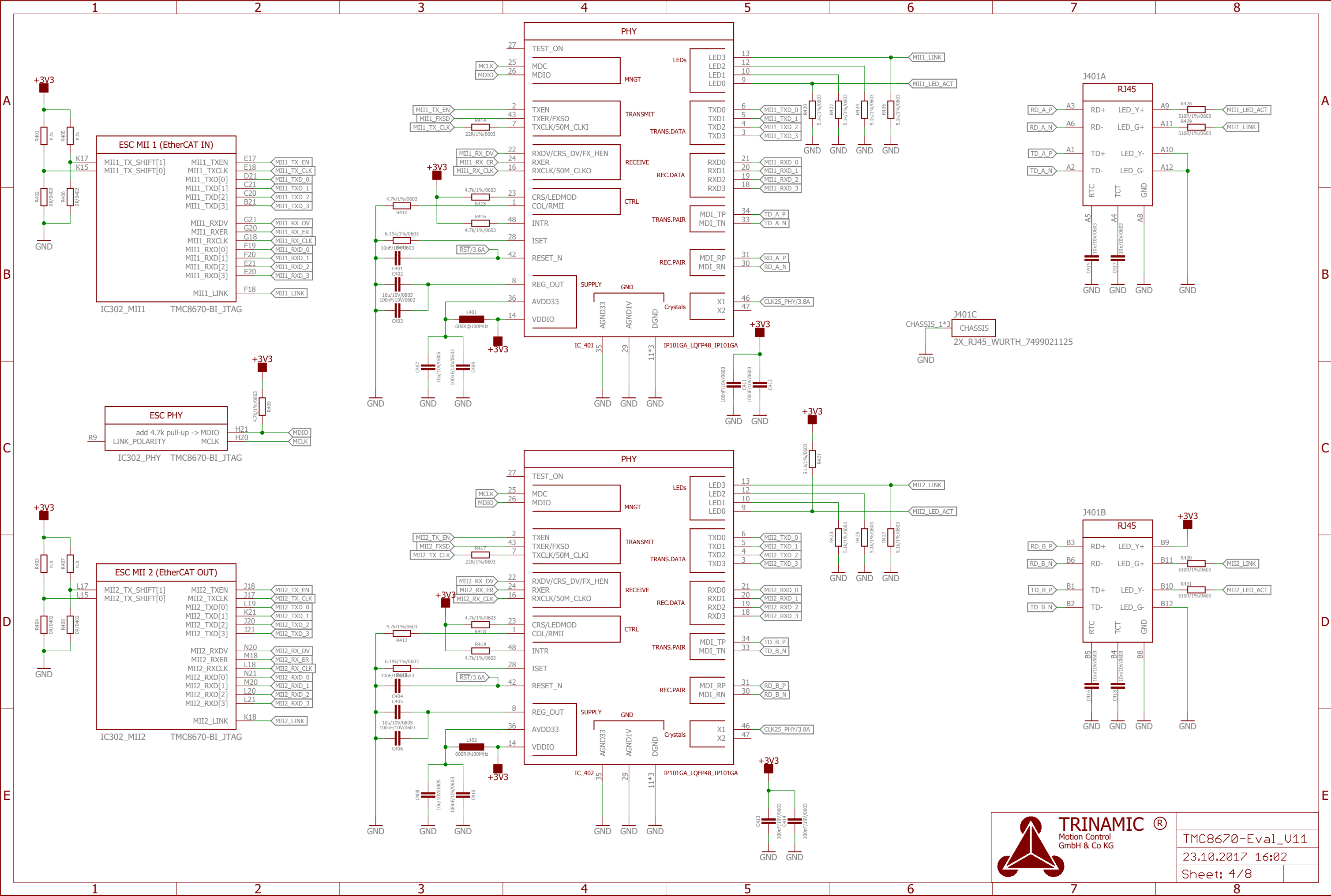


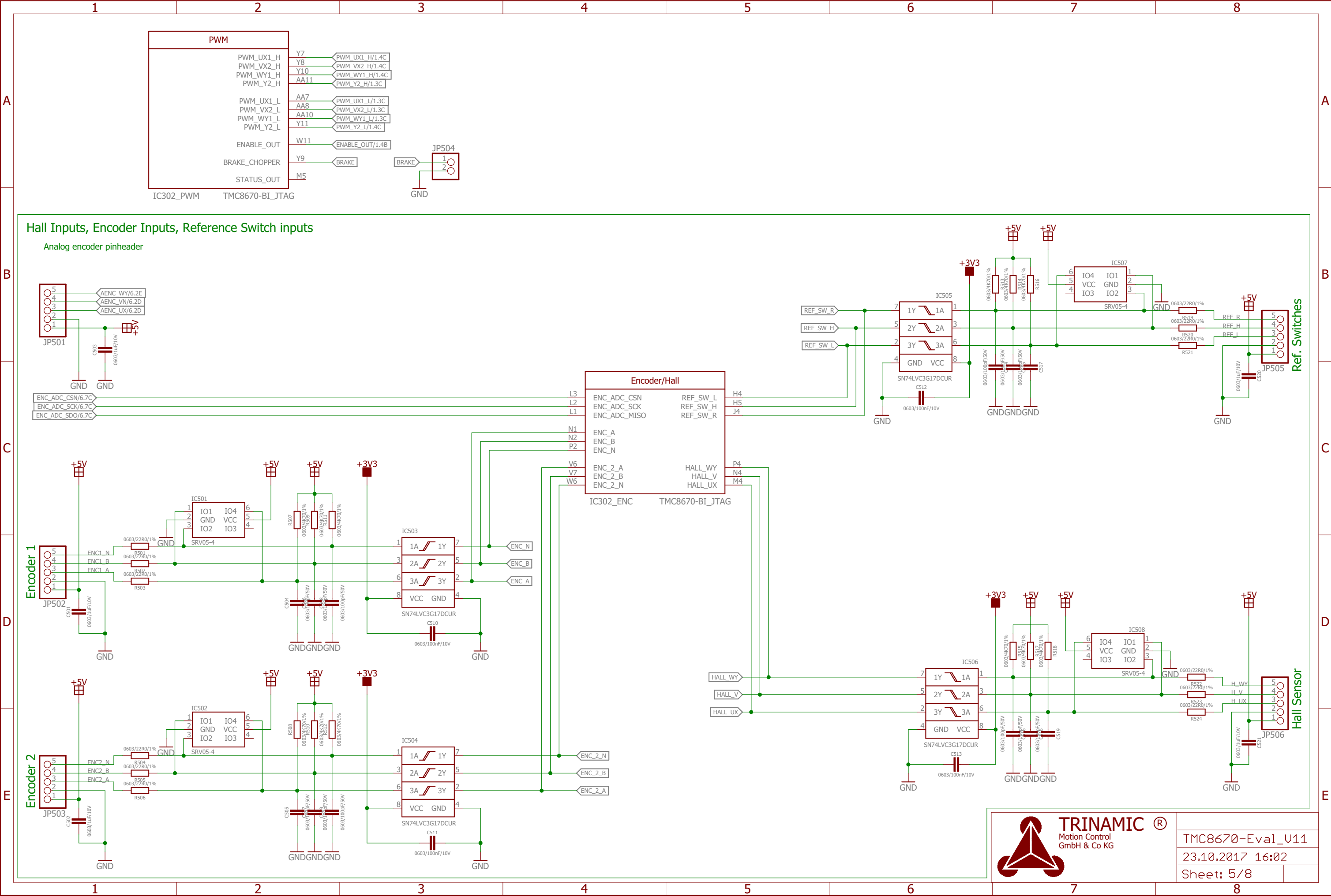
RESET circuit

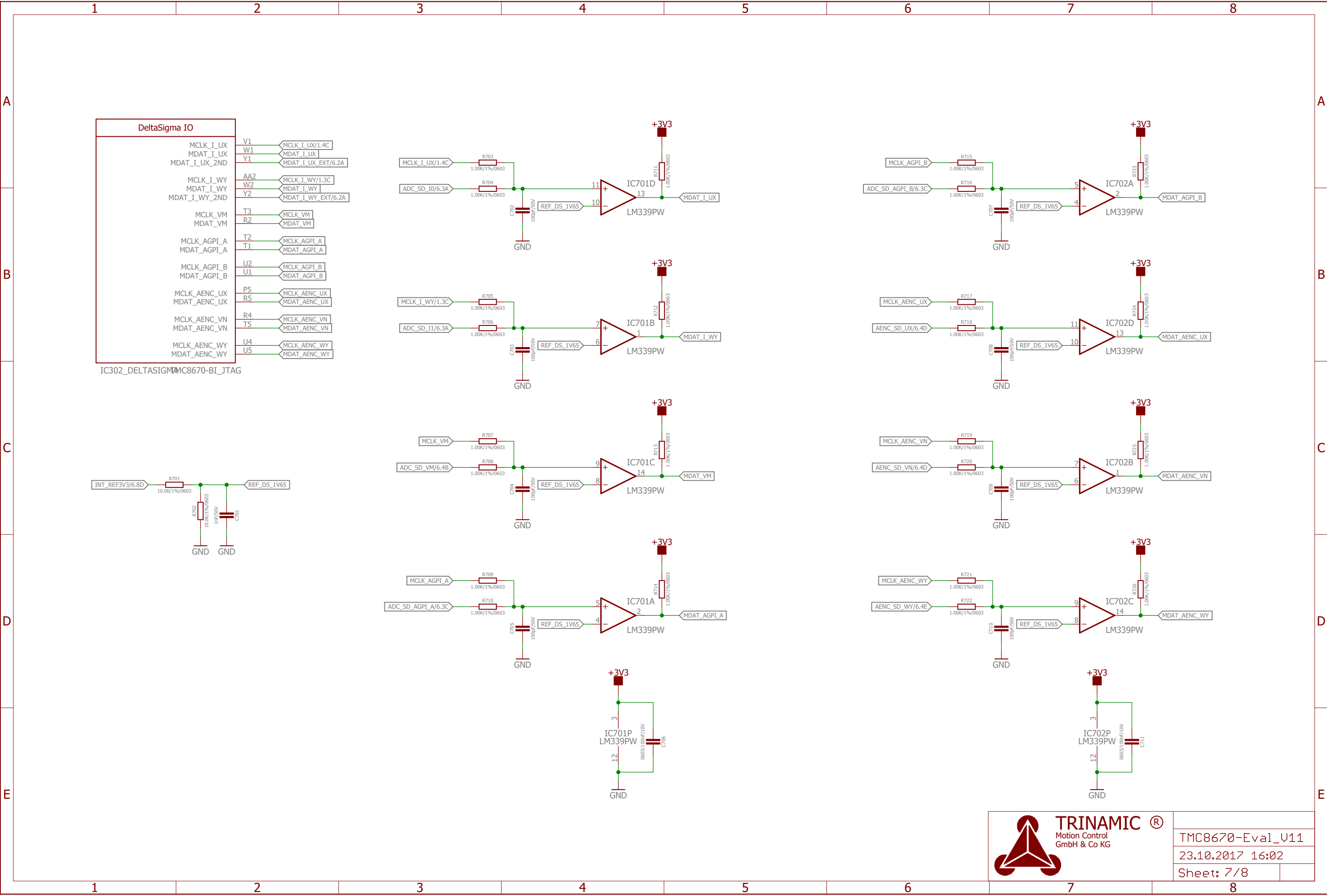


I2C EEPROM for EtherCAT (I2C address bits are b'000)









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